

**METHOD OF EXTRACTING PROPERTIES OF BACK END OF LINE (BEOL)****CHIP ARCHITECTURE****ABSTRACT OF THE DISCLOSURE**

5           A method for analyzing circuit designs includes  
discretizing a design representation into pixel elements  
representative of a structure in the design and  
determining at least one property for each pixel element  
representing a portion of the design. Then, a response of  
10 the design is determined due to local properties across the  
design.